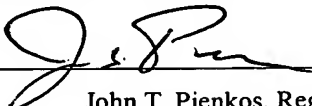


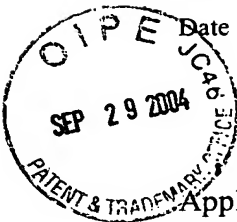
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John T. Pienkos, Reg. No. 42,997

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


Applicant(s): Sivaram Balasubramanian
Serial No.: 10/729,478
Filed: December 5, 2003
For: DISTRIBUTED REAL-TIME OPERATING SYSTEM
Docket No.: 99AB083 CIP

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is being presented for filing in the above case pursuant to Rules 97 and 98 of the Rules of Practice. A Form PTO/SB/08A "Information Disclosure Statement by Applicant" is attached which lists the references cited during the prosecution of the parent patent application of which the present Application is a continuation-in-part and claims the benefit (U.S. patent application No. 09/408,696 filed on September 30, 1999). The Applicant respectfully submits that, pursuant to 37 CFR 1.98(d), no copies of the references listed on Form PTO/SB/08A need be submitted to the Patent Office.

No additional fees for filing this paper are believed to be due. However, the Commissioner is hereby authorized to charge any additional fees due or to credit any overpayment to deposit account no. 17-0055.

Respectfully submitted,
Sivaram Balasubramanian

By: 

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>	Complete if Known	
	Application Number	10/729,478
	Filing Date	December 5, 2003
	First Named Inventor	Sivaram Balasubramanian
	Art Unit	2662
	Examiner Name	---
Sheet 1 of 2	Attorney Docket Number	99AB083 CIP

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 4,562,539	12-1985	Vince, Nigel L.	
		US- 5,136,718	08-1992	Haydt, Robert J.	
		US- 5,452,201	09-1995	Pieronek et al.	
		US- 5,530,643	06-1996	Hodorowski, John	
		US- 5,675,791	10-1997	Bhide et al.	
		US- 5,845,149	12-1998	Husted et al.	
		US- 5,884,046	03-1999	Antonov, Vadim	
		US- 5,896,289	04-1999	Struger, Odo J.	
		US- 5,949,673	09-1999	Struger et al.	
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		US- 6,182,120	01-2001	Beaulieu et al.	
		US- 6,370,572	04-2002	Lindskoog et al.	
		US- 6,381,652	04-2002	Reghunathan, Panchanathan	
		US-			
		US-			
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁵
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Application Number		10/729,478
	Filing Date		December 5, 2003
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	Examiner Name		---
	Attorney Docket Number		99AB083 CIP

Sheet

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OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Hamdaoui, M. et al "A Dynamic Priority Assignment Technique for Streams with (m,k) firm deadlines" IEEE Transactions on Computers, Dec. 1995, Vol. 44, Issue 12, pgs. 1443-1451	
		Ramanathan, P. "Overload Management in Real-Time Control Applications Using (m,k) Firm Guarantee" Parallel and Distributed Systems, IEEE, June 1999, Vol. 10, Issue 6, pgs. 549-559	
		Bonuccelli, M. A. et al "EDD Algorithm Performance Guarantee for Periodic Hard-Real-Time Scheduling in Distributed Systems Parallel and Distributed Processing, IEEE 12-16 April 1999 pgs. 668-677	
		Lee, J. K. et al "Real-Time Gang Scheduling with Workload Models for Parallel Computers" Parallel and Distributed Systems, IEEE, 14-16 Dec. 1998, pgs. 114-121.	
		Stewart, D.B. et al "Real-Time Scheduling of Dynamically Reconfigurable Systems" Systems Engineering, IEEE, 1-3 August 1991, pgs. 139-142.	
		Hou, C.J. et al "Allocation of Periodic Task Modules with Precedence and Deadline Constraints in Distributed Real-Time Systems" Real-Time Systems Symposium, 2-4 Dec. 1992, pgs. 146-155.	
		Yamane, S. "Hierarchical Design Method for Real-Time Distributed Systems" Real-Time Computing Systems and Applications 27-29 Oct. 1998, pgs. 189-192.	
		Fohler, G. et al "Static Scheduling of Pipelined Periodic Tasks in Distributed Real-Time Systems" Real-Time Systems, 11-13 June 1997, pgs. 128-135.	

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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